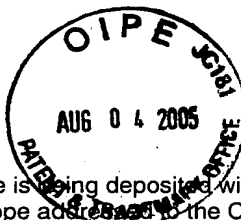


Docket No.: GR 00 P 1031



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AF

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

By: 

Date: August 2, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/760,405 Confirmation No: 1766  
Applicant : Xiaoning Nie  
Filed : January 12, 2001  
Title : Data-Processing Device for Processing Two or More  
Independent Processes in Parallel  
Art Unit : 2183  
Examiner : Aimee J. Li  
  
Docket No. : GR 00 P 1031  
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. 1.97(d)

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

International Publication WO 99/31580 (Akkary et al.), dated June 24, 1999;

Steven Wallace et al.: "Threaded Multiple Path Execution", 1998 IEEE, XP-002322675, 12 pgs.;

Quinn Jacobson et al. "Control Flow Speculation in Multiscalar Processors",  
Proceedings of the Third International Symposium on High Performance Computer  
Architecture, San Antonio TX, February 1-5, 1997, pp. 1-13;

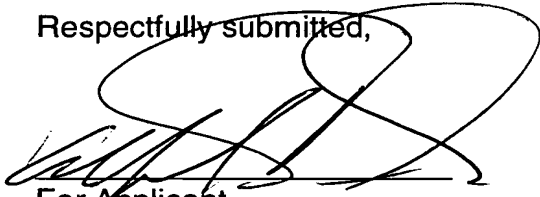
European Search Report dated June 27, 2005.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

In accordance with 37 C.F.R. 1.97 (d), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,



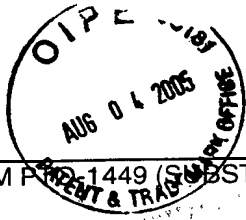
For Applicant

**Alfred K. Dassler**  
**52,794**

Date: August 1, 2005

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/am



FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: GR 00 P 1031  Applicant Xiaoning Nie  Filing Date January 12, 2001				Applic. No. 09/760,405  Group Art Unit 2183.0	
U.S. PATENT DOCUMENTS									
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE		
	A								
	B								
	C								
	D								
	E								
	F								
	G								
	H								
	I								
FOREIGN PATENT DOCUMENT									
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO		
	J	99/31580	06-1999	WIPO					
	K								
	L								
	M								
	N								
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)									
	O	Steven Wallace et al.: "Threaded Multiple Path Execution", 1998 IEEE, XP-002322675, 12 pgs.							
	P	Quinn Jacobson et al. "Control Flow Speculation in Multiscalar Processors", Proceedings of the Third International Symposium on High Performance Computer Architecture, San Antonio TX, February 1-5, 1997, pp. 1-13							
EXAMINER				DATE CONSIDERED					
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									